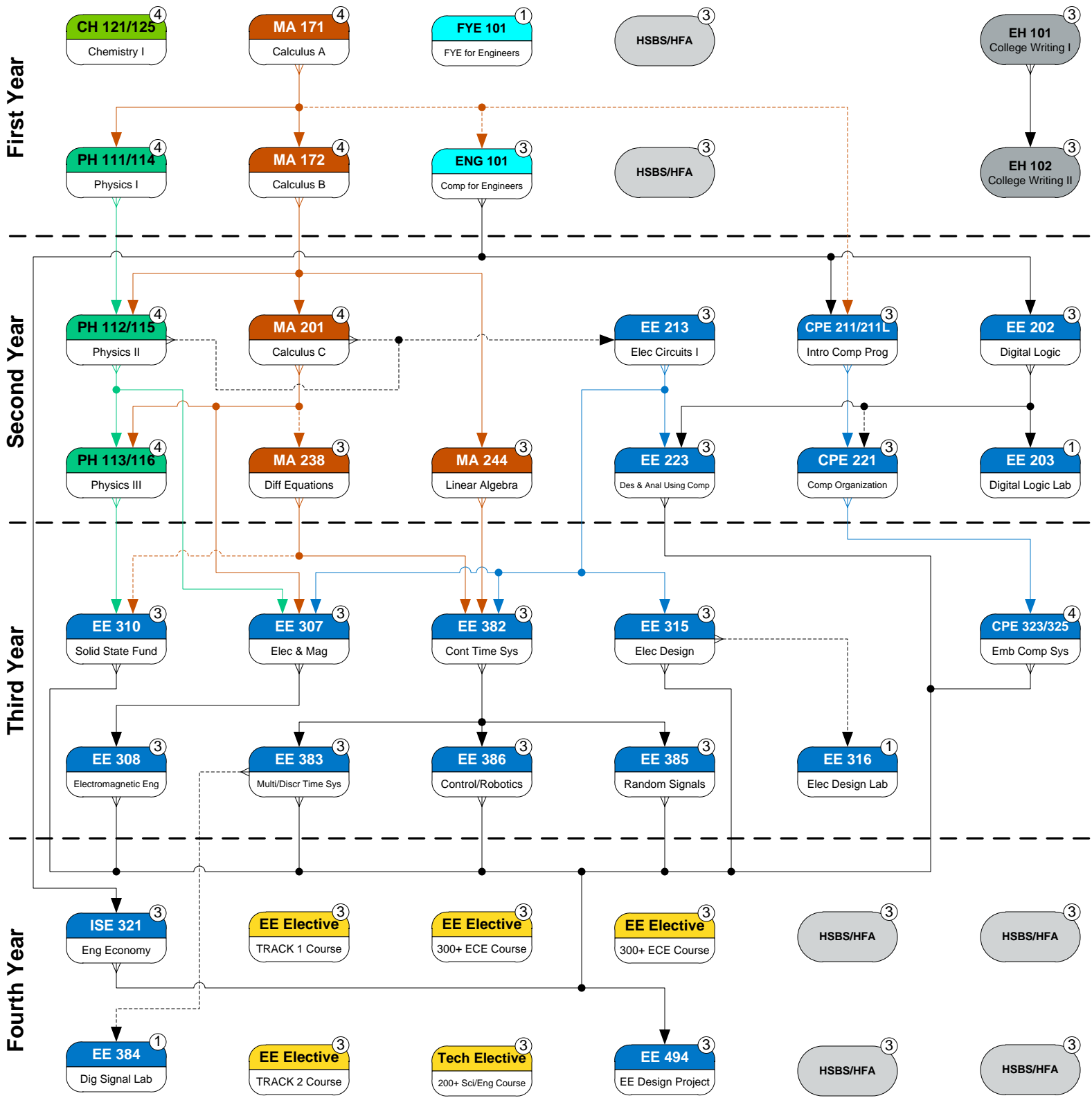


Student A#				Student Name (Last, First MI)		Offered: F=Fall S=Spr M=Sum
Semester, Transfer or AP	Grade	Course Number	Cr Hrs	Course Title	Prerequisites, Corequisites and/or Prerequisites with Concurrency	
English - 6 hours						
		EH 101	3	College Writing I	Placement	FSM
		EH 102	3	College Writing II	EH 101	FSM
Mathematics - 18 hours						
		MA 171	4	Calculus A	MA 113 or Level III Placement	FSM
		MA 172	4	Calculus B	MA 171	FSM
		MA 201	4	Calculus C	MA 172	FSM
		MA 238	3	Applied Differential Equations	Prereq w/Con: MA 201	FSM
		MA 244	3	Introduction to Linear Algebra	MA 172	FSM
Chemistry - 4 hours						
		CH 121	3	General Chemistry I	Plcmt or Prereq w/Con: MA 113	FSM
		CH 125	1	General Chemistry Lab I	Prereq w/Con: CH 121	FSM
Physics - 12 hours						
		PH 111	3	General Physics w/Calculus I	MA 171; Coreq: 114	FSM
		PH 114	1	General Physics Lab I	Prereq w/Con: PH 111	FSM
		PH 112	3	General Physics w/Calculus II	MA 172, PH 111, PH 114; Coreq: 115	FSM
		PH 115	1	General Physics Lab II	Coreq: PH 112	FSM
		PH 113	3	General Physics w/Calculus III	MA 172, PH 112, PH 115; Coreq: 116	FSM
		PH 116	1	General Physics Lab III	Coreq: PH 113	FSM
History, Social & Behavioral Sciences, Humanities & Fine Arts - 18 hours						
			3	History	HY 103, HY 104, HY 221, or HY 222	FSM
			3	Literature	EH 207 or EH 208	FSM
			3	Fine Art	ARH 100, ARH 101, ARH 103, TH 122, MU 100, or ARS 160	FSM
			3	Social & Behavioral Science	For more information on HSBS/HFA Requirements: http://www.uah.edu/eng/departments/undergraduate-engineering/student-forms	FSM
			3	Sequence Course (HY or EH)		FSM
			3	HSBS/HFA		FSM
First-Year Engineering - 4 hours						
		FYE 101	1	First-Year Experience for Engineers	None	FS
		ENG 101	3	Computing for Engineers	Prereq w/Con: MA 171	SM
**	Class has required lab section			Electrical Engineering - 52 hours		
		EE 202	3	Intro to Digital Logic Design	ENG 101	FSM
		EE 203	1	Digital Logic Design Lab	EE 202	FSM
**		CPE 211	3	Intro to Computer Programming in Engineering	ENG 101, Prereq w/Con: MA 171; Coreq: CPE 211L	FSM
		EE 213	3	Electrical Circuit Analysis I	Prereq w/Con: PH 112, MA 201	FSM
		CPE 221	3	Computer Organization	CPE 211; Prereq w/Con: EE 202	FSM
		EE 223	3	Design and Analysis using Computing	EE 202, EE 213	FS
		EE 307	3	Electricity and Magnetism	PH 112, MA 201, EE 213	FSM
		EE 308	3	Electromagnetic Engineering	EE 307	FS
		EE 310	3	Solid State Fundamentals	PH 113; Prereq w/Con: MA 238	FS
		EE 315	3	Introduction to Electronic Analysis and Design	EE 213	FSM
		EE 316	1	Electronic Measurements & Devices Design Lab	Prereq w/Con: EE 315	FS
		ISE 321	3	Engineering Economy	ENG 101	FSM
		CPE 323	3	Intro to Embedded Computer Systems	CPE 221; Coreq: CPE 325	FS
		CPE 325	1	Lab Component of Intro of CE 323	Coreq: CPE 323	FS
		EE 382	3	Analytical Meth for Continuous Time Sys	EE 213, MA 238, MA 244	FSM
		EE 383	3	Analytical Meth for Mult and Discr Time Sys	EE 382	FSM
		EE 384	1	Digital Signal Processing Laboratory	CPE 381 or Prereq w/Con:EE 383	FS
		EE 385	3	Random Signals and Noise	CPE 381 or EE 382	FSM
		EE 386	3	Introduction to Control and Robotic Systems	CPE 381 or EE 382	FSM
		EE 494	3	EE Design Projects	EE 223, EE 308, EE 310, EE 315, EE 383, EE 385, EE 386, CPE 323, ISE 321	FS
Electrical Engineering Electives - 12 hours						
			3	T1	Track: _____	
			3	T2	(Must take a 2 course track in one area.)	
			3		300+ Level ECE course	
			3		300+ Level ECE course (May take CPE 212 for credit)	
Technical Elective - 3 hours						
			3		200+ Level Science or Engineering course	

All prerequisite classes must be completed with a "C-" or higher grade.
The Catalog is the final authority for all degree requirements.

Academic Flowchart

Electrical Engineering 2017/2018 (129 Hours)



Legend Updated: 5/31/17	Mathematics	First-Year Engineering	Freshman Composition	Credit Hours
	Physics	Electrical Engineering Option	History, Social & Behavioral Science Humanity & Fine Art	Prerequisite
	Chemistry	Engineering Electives		Prereq w/concurrency

Electrical and Computer Engineering Department: 4-Year Rolling Class Schedule, Fall 2017 - Spring 2021*

	Fall 2017	Anticipated Sections	Spring 2018	Anticipated Sections	Fall 2018	Spring 2019	Fall 2019	Spring 2020	Fall 2020	Spring 2021
CPE 211** Intro to Comp Prog in Eng	Y	2	Y	2	Y	Y	Y	Y	Y	Y
CPE 212 Fund of Software Eng	Y	2	Y	1	Y	Y	Y	Y	Y	Y
CPE 221 Computer Organization	Y	1	Y	1	Y	Y	Y	Y	Y	Y
CPE 322 Digital Hardware Design	N	0	Y	1	N	Y	N	Y	N	Y
CPE 323 Intro Embedded Comp Sys	Y	1	E	1	Y	E	Y	E	Y	E
CPE 324 Digital Hardware Des Lab	N	0	Y	4	N	Y	N	Y	N	Y
CPE 325 Embedded Comp Sys Lab	Y	8	E	6	Y	E	Y	E	Y	E
CPE 353 Software Design/Eng	Y	2	N	0	Y	N	Y	N	Y	N
CPE 381 Fund of Signals/Systems	Y	1	Y	1	E	Y	E	Y	E	Y
CPE 412 Intro to Parallel Prog	Y	1	N	0	Y	N	Y	N	Y	N
CPE 423 Hardwr/Softwr Co-Design	N	0	D	0	D	D	D	D	D	D



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CPE 426 Modeling/Synthesis	N	0	D	0	D	D	D	D	D	D
CPE 427** VLSI Design I	Y	1	N	0	D	D	D	D	D	D
CPE 428** VLSI Design II	N	0	D	0	D	D	D	D	D	D
CPE 431 Intro to Comp Architecture	Y	1	N	0	Y	N	Y	N	Y	N
CPE 434 Operating Systems	N	0	Y	1	N	Y	N	Y	N	Y
CPE 435 Operating Systems Lab	N	0	Y	2	N	Y	N	Y	N	Y
CPE 436 Internals of Mod Oper Sys	N	0	N	0	N	Y	N	N	N	Y
CPE 438 Real Time/Embedded Sys	N	0	D	0	D	D	D	D	D	D
CPE 448 Intro to Comp Networks	Y	1	Y	1	E	Y	E	Y	E	Y
CPE 449** Intro to Info Assurance Eng	Y	1	N	0	Y	N	Y	N	Y	N
CPE 453 Senior Software Studio	N	0	D	0	D	D	D	D	D	D
CPE 495 Comp Eng Design I	Y	1	N	0	Y	N	Y	N	Y	N
CPE 496 Comp Eng Design II	N	0	Y	2	N	Y	N	Y	N	Y



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EE 202 Intro to Digital Logic Des	Y	2	Y	1	Y	Y	Y	Y	Y	Y
EE 203 Digital Logic Design Lab	Y	3	Y	4	Y	Y	Y	Y	Y	Y
EE 213 Electrical Circuit Analysis I	Y	4	Y	4	Y	Y	Y	Y	Y	Y
EE 223 Design/Analysis using Comp	Y	2	Y	3	N	Y	Y	Y	Y	Y
EE 307 Electricity/Magnetism	Y	1	E	1	Y	E	Y	E	Y	E
EE 308 Electromagnetic Eng	Y	1	Y	1	E	Y	E	Y	E	Y
EE 310 Solid State Fundamentals	Y	1	E	1	Y	E	Y	E	Y	E
EE 315 Intro Elec Analysis/Design	Y	1	E	1	Y	E	Y	E	Y	E
EE 316 Electronic Meas/Devices Design Lab	Y	5	Y	5	E	Y	E	Y	E	Y
EE 382 Analytical Meth Contin Time Sys	Y	1	E	1	Y	E	Y	E	Y	E
EE 383 Analytical Meth Multi/Discr Time Sys	Y	1	Y	1	E	Y	E	Y	E	Y
EE 384 Digital Signal Processing Lab	Y	3	Y	3	E	Y	E	Y	E	Y
EE 385 Random Signals/Noise	Y	1	Y	1	E	Y	E	Y	E	Y



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EE 386 Intro to Control/Robotic Systems	Y	1	Y	1	E	Y	E	Y	E	Y
EE 401 Digital Signal Proc Architectures	N	0	D	0	D	D	D	D	D	D
EE 411 Electric Power Systems	Y	1	D	0	D	D	D	D	D	D
EE 414 Analog and Digital Filter Design	N	0	Y	1	N	Y	N	Y	N	Y
EE 416 Electronics II	N	0	Y	1	N	Y	N	Y	N	Y
EE 423 Communication Sys/Simulation	N	0	D	0	D	D	D	D	D	D
EE 424 Intro to Data Comm Networks	Y	1	N	0	Y	N	Y	N	Y	N
EE 426 Communication Theory	N	0	Y	1	N	Y	N	Y	N	Y
EE 427** VLSI Design I <i>Xlisted CPE 427</i>	Y	1	N	0	D	D	D	D	D	D
EE 428** VLSI Design II	N	0	D	0	D	D	D	D	D	D
EE 436 Digital Electronics	Y	1	N	0	Y	N	Y	N	Y	N
EE 437 Electr Manufacturing Processes	N	0	N	0	N	N	N	N	N	N
EE 451 Optoelectronics	Y	1	N	0	Y	N	Y	N	Y	N



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EE 453 Laser Systems	Y	1	N	0	Y	N	Y	N	Y	N
EE 454 Optical Fiber Communications	N	0	Y	1	N	Y	N	Y	N	Y
EE 486 Intro to Modern Control Systems	Y	1	N	0	D	D	D	D	D	D
EE 494 EE Design Projects	Y	1	Y	1	E	Y	E	Y	E	Y
OPE 441 Optical Systems Design	N	0	D	0	D	D	D	D	D	D
OPE 442 Interference and Diffraction	N	0	D	0	D	D	D	D	D	D
OPE 451 Optoelectronics	Y	1	N	0	Y	N	Y	N	Y	N
OPE 453 Laser Systems	Y	1	N	0	Y	N	Y	N	Y	N
OPE 454 Optical Fiber Communications	N	0	Y	1	N	Y	N	Y	N	Y
OPE 456 Photonics Lab	N	0	Y	1	N	Y	N	Y	N	Y
OPE 459 Optical Engineering Design I	Y	1	N	0	Y	N	Y	N	Y	N
OPE 460 Optical Engineering Design II	N	0	Y	1	N	Y	N	Y	N	Y

Legend

Y
E

Course will be offered in designated term.

Course will likely be offered in designated term, but availability will be determined by faculty availability and budget.



N

Course will not be offered in designated term.

D

Course may be made available given appropriate demand or interest.

*

UAH College of Engineering will make every effort to adhere to the class plan schedule, but it reserves the right to make necessary adjustments based on budget and faculty availability.

**

Course has a required lab section.